	IN THE UNITED STATES PATENT AND	TRADEMAR	K OFFICE
Applicant:	Clynes, et al.	Docket No:	TI-32423
Serial No:	09/862,523	Examiner:	Moe, Aung Soe
Filed:	5/22/2001	Art Unit:	2685
For:	ON-CHIP 2D ADJUSTABLE DEFECTIVE PIX		G FOR CMOS IMAGERS

NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF APPEALS

Assistant Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

	CERTIFICATION OF FACSIMILE TRANSMISSION I hereby
	certify that the following papers are being transmitted by
	facsimile to the U.S. Patent and Trademark Office at 571-
	273-8300 on the date shown below.
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	tomme kambers
(I	
Ч	Tommie Chambers 12-23-05
	19-93-02

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DEC 2 3 2005

Dear Sir:

ς,

Applicant hereby appeals to the Board of Appeals from the decision mailed September 9, 2005, of the Primary Examiner finally rejecting Claims 1, 3-13, 17, 18, and 20.

The item(s) checked below are appropriate:

1. <u>X</u> An extension of time to respond to the final rejection

____ was granted on _____

X is requested for <u>1</u> month.

- 2. <u>A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.</u>
- 3. <u>X</u> Fee \$500.00:
 - _ Not required (Fee paid in prior appeal)
 - X The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. This form is submitted in duplicate.

12/27/2005 EAREGAY1 00000104 09862523

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Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265 (972) 917-5633 W. Daniel Swayze, Jr. Attorney for Appellant Reg. No. 34,478

PAGE 2/2 * RCVD AT 12/23/2005 12:21:37 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-6/33 * DNIS:2738300 * CSID:972 917 4417 * DURATION (mm-ss):01-04

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